

<b>FORM PTO-1449 (SUBSTITUTE)</b>  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE          STATEMENT BY APPLICANT</b> (37 CFR 1.98(b))				Attorney Docket No.: P2001,0377 Appl. No.: <u>10/724905</u>  Applicant: JUDITH MAGET  Filing Date: December 1, 2003 Group Art Unit:			
EXAMINER INITIALS	PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE	
<i>Ch</i>	A	6,100,770	8/8/00	Litwin et al.	<del>          </del>		
<i>Ch</i>	B	5,965,912	10/12/99	Stolfa et al.	<del>          </del>		
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<b>FOREIGN PATENT DOCUMENT</b>							
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO	
<i>Ch</i>	J	2 138 206 A	10/17/84	U.K.			
<i>Ch</i>	K	03147376 A	6/24/91	Japan			
<i>Ch</i>	L	1 024 538 A1	8/2/00	Europe			
<i>Ch</i>	M	0 902 483 A1	3/17/99	Europe			
	N						
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
<i>Ch</i>		Burghartz, J. N. et al.: "Integrated RF and Microwave Components in BiCMOS Technology", IEEE Transactions on Electron Devices, Vol. 43, No. 9, September 1996, pp. 1559-1570					
<i>Ch</i>		Maget, J. et al.: "A Varactor with High Capacitance Tuning Range in Standard 0.25um CMOS Technology", 4 pages					
EXAMINER <i>JGL</i>				DATE CONSIDERED <i>8-19-04</i>			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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<i>a</i>		Wong, W. M. Y. et al.: "A Wide Tuning Range Gated Varactor", IEEE Journal of Solid-State Circuits, Vol. 35, No. 5, May 2000, pp. 773-779					
<i>a</i>		Svelto, F. et al.: "A Three Terminal Varactor for RFIC's in Standard CMOS Technology", IEEE Transactions on Electron Devices, Vol. 47, No. 4, April 2000, pp. 893-895					
EXAMINER <i>Regy</i>				DATE CONSIDERED <i>8-19-04</i>			
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<i>on</i>		Tiebout, M.: "A Fully Integrated 1.3GHz VCO for GSM in 0.25µm Standard CMOS with a Phasenoise of -142Bc/Hz at 3MHz Offset", European Microwave Week, 2000, 4 pages					
<i>on</i>		Andreani, P. et al.: "On the Use of MOS Varactors in RF VCO's", IEEE Journal of Solid-State Circuits, Vol. 35, No. 6, June 2000, pp. 905-910					
EXAMINER <i>Regy</i>				DATE CONSIDERED <i>08-19-04</i>			
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
		Porret, A.-S. et al.: "Design of High-Q Varactors for Low-Power Wireless Applications Using a Standard CMOS Process", IEEE Journal of Solid-State Circuits, Vol. 35, No. 3, March 2000, pp. 337-345

  

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